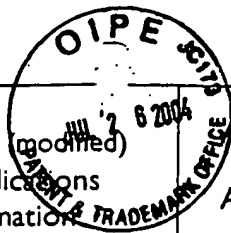


Form PTO-1449 (modified)

List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)



ATTY. DOCKET NO: BP1735CON

APPLICANT: JOSEPH B. ROLLANDS

FILING DATE: 12/30/2003

PAGE 1 OF 6

SERIAL NO: 10/748,551

GROUP: 2186

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
ZL	A1	3,693,165	9/1972	Reiley et al.			
	A2	5,416,783	5/1995	Broseghini et al.			
	A3	5,450,551	9/1995	Amini et al.			
	A4	4,511,994	4/1985	Webb et al.			
	A5	4,633,440	12/1986	Pakulski			
	A6	4,654,778	3/1987	Chiesa et al.			
	A7	4,907,278	3/1990	Cecinati et al.			
	A8	5,241,663	8/1993	Rohwer			
	A9	5,325,504	6/1994	Tipley et al.			
	A10	5,317,716	5/1994	Liu			
	A11	5,510,934	4/1996	Brennan et al.			
	A12	5,551,001	8/1996	Cohen et al.			
	A13	5,592,679	1/1997	Yung			
	A14	5,644,752	7/1997	Cohen et al.			
	A15	5,668,815	9/1997	Gittinger et al.			
	A16	5,668,972	9/1997	Liu et al.			
	A17	5,748,640	5/1998	Jiang et al.			
	A18	5,768,555	6/1998	Tran et al.			
	A19	5,784,588	7/1998	Leung			
	A20	5,802,338	9/1998	Rechtschaffen et al.			
	A21	5,809,528	9/1998	Miller et al.			
↓	A22	6,161,167	12/2000	Witt			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

ZL	A23	Dekker et al., "A Realistic Fault Model and Test Algorithms for Static Random Access Memories," IEEE Transactions on Computer-Aided Design, Vol. 9, No. 6, June 1990, pp. 567-572.
	A24	Halfhill, "SiByte Reveals 64-bit Core for NPUs," Microprocessor Report, June 2000, pp. 45-48.
	A25	Cyrix® 5x86 Microprocessor, July 1995, 8 pgs.
↓	A26	Cyrix® 6x86 Microprocessor, August 1995, 6 pgs.

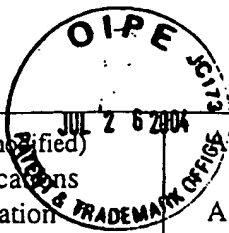
EXAMINER:

/Zhuo Li/

DATE CONSIDERED:

05/09/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.



PAGE 3 OF 6

Form PTO-1449 (modified)

List of Patents and Publications

For Applicant's Information

Disclosure Statement

(Use several sheets if necessary)

ATTY. DKT. NO. BP1735 CON

APPLICANT: JOSEPH B. ROWLANDS

FILING DATE: 12/30/2003

SERIAL NO. 10/748,551

GROUP: 2186

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
ZL	B29	0 061 570	10/1982	EP			
	B30	0 259 095	3/1988	EP			
	B31	0 381 471	8/1990	EP			
	B32	0 436 092	7/1991	EP			
	B33	0 459 232	12/1991	EP			
	B34	0 459 233	12/1991	EP			
	B35	0 568 221	11/1993	EP			
	B36	0 687 979	12/1995	EP			
	B37	2 263 985	8/1993	GB			
	B38	2 263 987	8/1993	GB			
	B39	2 281 422	3/1995	GB			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

ZL	B40	Intel® StrongARM® SA-1100 Microprocessor, Developer's Manual, August 1999, © Intel Corporation, Ch. 1, p. 6; Ch. 2, p. 2; Ch. 6, pp. 2-5; Ch. 7, p. 3.
	B41	"Memory Arbitration with Out of Order Execution in Conjunction with a RISC System," IBM Technical Disclosure Bulletin, September 1992, pp. 62-64.
	B42	"Handling Reservations in Multiple-Level Cache," IBM Technical Disclosure Bulletin, December 1993, pp. 441-446.
	B43	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2 000, 15 pages.

EXAMINER:

/Zhuo Li/

DATE CONSIDERED:

05/09/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

FILING DATE: 12/30/2003

[illegible]

Information Disclosure Statement--PTO 1449 (modified)

Form PTO-1449 (modified)
List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

ATTY. DKT. NO. BP1735 CON

SERIAL NO. 10/748,551

APPLICANT: JOSEPH B. ROWLANDS

GROUP: 2186

FILING DATE: 12/30/2003

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
ZL	D1	5,493,667	2/20/96	Huck, et al.			
	D2	5,586,293	12/17/96	Baron, et al.			
	D3	5,809,531	9/15/98	Brabandt			
	D10	5,983,321	11/9/99	Tran, et al.			
	D11	5,671,444	9/23/97	Akkary, et al.			
	D12	6,269,427	7/31/01	Kuttanna, et al.			
	D13	6,295,608	9/25/01	Parkes, et al.			

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
ZL	D4	325 420	7/26/89	EP			
	D5	997 821	5/3/00	EP			
	D6	2 214 336	8/31/89	GB			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

ZL	D7	Gallup, et al., "Testability Features of the 68040," Motorola, Inc., 1990 International Test Conference, IEEE, pp. 749-757.
ZL	D8	Hollenbeck, et al., "PA7300LC Integrates Cache for Cost/Performance," Hewlett Packard Company, IEEE, 1996, pp. 167-174.
ZL	D9	International Search Report for EP 26 231-50 mailed February 21, 2003, 7 pages.

EXAMINER:

/Zhuo Li/

DATE CONSIDERED:

05/09/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.



Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Subsistence ~~1406~~ 1449/PTO

(Use as many sheets as necessary)

Sheet	6	of	6
-------	---	----	---

Complete if Known

Application Number	10/748,551
Filing Date	12/30/2003
First Named Inventor	Joseph B. Rowlands
Art Unit	2186
Examiner Name	
Attorney Docket Number	BP1735CON

U. S. PATENT DOCUMENTS

[illegible]

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.